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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Zahi M. Kurzum et al.

Examiner: Nghia M. Doan

Application No.: 10/766,549

Group Art Unit: 2825

Filed: January 27, 2004

For: A METHOD FOR LEGALIZING THE

PLACEMENT OF CELLS IN AN INTEGRATED CIRCUIT LAYOUT Date: January 25, 2006

INFORMATION DISCLOSURE STATEMENT

The Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. § 1.56, and in accordance with the provisions of 37 C.F.R. §§ 1.97 and 1.98, the undersigned attorney respectfully submits copies of the US and non-US patent and publications as listed on Form PTO-1449, attached hereto.

No representation is made or intended as to the pertinency of the disclosed information, that information more pertinent than that listed is not available, or that other information is not applicable.

FIS9-2003-0418-US1

A fee of \$180.00 is believed to be due for this submission. The Commissioner is hereby authorized to charge such fees and any additional fees to Deposit Account No. 09-0458.

The applicant's undersigned attorney may be reached by telephone at (845) 894-2481. All correspondence regarding this Information Disclosure Statement should be directed to the below listed address.

Respectfully submitted,

H. Daniel Schnurmann Agent for Applicants Registration No. 35,791

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Docket Number (Optional) Application Number FIS9-2003-0418-US1 10/766,549 MATION DISCLOSURE CITATION Applicant(s) Kurzum et al. (Use several sheets if necessary) Filing Date **Group Art Unit** 01/27/2004 2825 U.S. PATENT DOCUMENTS FILING DATE DOCUMENT NUMBER DATE NAME CLASS SUBCLASS INITIAL IF APPROPRIATE 5,619,419 04/08/1997 D'Haeseleer et al. 5,943,243 08/24/1999 Sherlekar et al. U.S. PATENT APPLICATION PUBLICATIONS *EXAMINER FILING DATE DOCUMENT NUMBER DATE INITIAL NAME CLASS SUBCLASS IF APPROPRIATE FOREIGN PATENT DOCUMENTS Translation REF DOCUMENT NUMBER DATE COUNTRY CLASS SUBCLASS NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) M.A. Breuer, "Min-cut Placement"; IEEE Design Automation and Fault-Tolerant Computing, pp. 343-382, October 1977 H. Yang et al., "Efficient network flow based min-cut balanced partitioning"; Proceedings of the IEEE/ACM Int. Conf. Computer-Aided Design, pp. 50-55, 1994 **EXAMINER** DATE CONSIDERED EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

OIA		FIS9-2003-0418-US1	Application Number 10/766,549	
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